Understanding Interactions Between Chip Architecture and Uncertainties in Semiconductor Supply and Demand

Ramakrishna Kanungo kanungo3@illinois.edu Muhammad Husnain Mubarik mubarik3@illinois.edu Swamynathan Siva siva3@illinois.edu Lav Varshney varshney@illinois.edu Nathaniel Bleier nbleier3@illinois.edu

Rakesh Kumar rakeshk@illinois.edu

ABSTRACT

Mitigating losses from supply and demand volatility in the semiconductor supply chain and market has traditionally been cast as a logistics and forecasting problem. We investigate how the architecture of a family of chips influences how it is affected by supply and demand uncertainties. We observe that semiconductor supply chains become fragile, in part, due to single demand paths, where one chip can satisfy only one demand. Chip architects can enable multiple paths to satisfy a chip demand, which improves supply chain resilience. Based on this observation, we study composition and adaptation as architectural strategies to improve resilience to volatility and also introduce a third strategy of dispersion. These strategies allow multiple paths to satisfy a given chip demand. We develop a model to analyze the impact of these architectural techniques on supply chain costs under different regimes of uncertainties and evaluate what happens when they are combined. We present several interesting and even counterintuitive observations about the product configurations and market conditions where these interventions are impactful and where they are not. In all, we show that product redesign supported by architectural changes can mitigate nearly half of the losses caused by supply and demand volatility. As far as we know, this is the first such investigation concerning chip architecture.

1. INTRODUCTION

Chip shortages are front and center these days. Mismatches between supply and demand continue to wreak havoc on our lives—affecting the supply of computers [61] and automobiles [49], and are even implicated in inflation [52].

While public awareness of supply and demand swings affecting the semiconductor industry is at an all-time high, the problem is not new. Figure 1a shows the number of PCs shipped per quarter by Hewlett-Packard, ASUS, and the market as a whole [8,9,10,11,12,13,14,15,16,17,18,19,20,21]. We take the average of HP shipments and scale up/scale down the ASUS shipments and Total PC Market shipment volumes to that value. This is to visualize the amount of variance in the sales numbers on a similar scale. The shipping volumes in the overall PC industry show a standard deviation of 27.8% of the mean over the past four years. Notice individual vendors show more variance than the whole industry (11% additional standard deviation). Variance in semiconductor revenue over the last 30 years [22] (Figure 1c) illustrates the same issue. Averaged over 30 years, revenue varies over a moving average forecast with a standard deviation of 12% of profit, and individual years see deviations of more than 25%. The fraction of foundry capacity used [5] also varies greatly due to market volatility.

Supply and demand volatility has only worsened in recent years as factors of uncertainty in the semiconductor industry are intensifying. On the supply side, climate change is increasing the chance of fires, droughts and winter storms that affect foundries [67], [83]; rising geopolitical tensions [76] [60] are raising the chance of trade embargoes that disconnect entire markets/suppliers from the world system; and increasingly complex designs and lithography techniques are steadily reducing the number of facilities that can make cutting-edge chips [71]. Demand for chips is also increasingly volatile as they are used in more and more applications that, with little substitutability among applications (e.g., spike in demand of GPUs when cryptocurrency values surge), cannot be fulfilled by slump or average demand in another application. This volatility may have a steep cost, as Table 1 shows for some recent unforeseen events.

How can this problem be addressed? Given the inherent characteristics of semiconductors and their production (short product life times [84], long lead times [77], etc.) and the fact that modifying semiconductor production takes several years [58], little can be done in the short term to improve semiconductor supply. While forecasts of demand can help mitigate the effects of demand volatility, forecasting market behavior is difficult. Figure 1b compares projections from top analytic firms for total yearly semiconductor revenue to actual revenue in 2018–2021 [1, 26, 27, 28, 29, 30, 31, 65], showing differences of 30–40 billion dollars per year, despite projections made in the middle of the year. The recent glut of chips in the market [81] also illustrates the failure of current approaches to mitigate supply and demand volatility.

So if typical ways to address the problem do not work, what can be done? Much previous work [74] suggests finding the right supply chain for the product characteristics. In this paper, we turn that on its head to explore the right product characteristics (architectures) for the supply-demand chain,

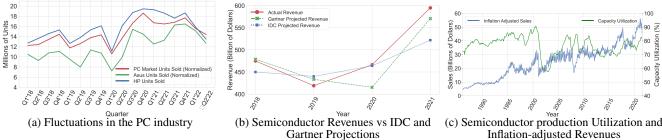


Figure 1: Semiconductor industry trends

Inflation-adjusted Revenues

and ask the question-What role does chip architecture have in mitigating effects of supply and demand volatility?

We develop a model (Section 3) to analyze the impact of different architectural techniques on supply chain costs. Using model parameters derived from real-world data, we study the interactions between architecture and uncertainties in semiconductor supply and demand. We believe our model, rooted in consultation with American and Korean industry practitioners [59], is sufficiently robust to yield several interesting and sometimes counter-intuitive conclusions, since we focus on comparisons and trends rather than absolute numbers.

We show (Section 5) that a one-to-one chip-to-demand mapping, as is common today, is a significant source of fragility, causing over three-fourth reduction in profits under our estimates of industry uncertainty. We show that one architectural strategy to mitigate this, composition, has limited efficacy in improving profits beyond the yield savings it offers as it is currently done, showing only a small benefit under supply volatility. A new optimized approach to composition, however, mitigates up to 33% of the losses from demand volatility. We show another architectural solution to eliminate one-to-one chip-to-demand mapping, adaptation, can mitigate 20% of losses from demand uncertainty but is unable to mitigate losses from supply volatility. A new optimized form of adaptation doubles its effectiveness in demand-volatile situations and also provides some benefits in supply-volatile situations. We then explore an additional architectural mechanism- dispersion-that can nearly halve losses from supply volatility, and study its interactions with composition and adaptation. Finally, we model how knowledge of market behavior helps improve profits. We find that its benefits are less than most of our proposed interventions, and that these interventions continue to provide benefits when market mechanisms are known. In all, we show that nearly half of the losses from both supply and demand volatility can be mitigated by our techniques.

The main contributions of the paper are as follows.

- We perform the first study on the role of chip architecture in resilience against supply and demand volatility.
- We make a key observation (and demonstrate quantitatively) that a one-to-one mapping between produced and demanded chips leads to high costs from supply and demand disruptions.
- We develop a model that allows us to analyze the impact of different architectural techniques on supply chain

Table 1: Unforecasted events and resulting loss to foundry

Loss to Foundry (\$)
28-35M (30-40K wafers)
286-357M (~71K wafers)
~100M
171M
~4.255B loss just from AMD
60B
13% TSMC's revenue ~= 455M
100M

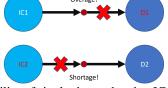


Figure 2: Fragility of single-demand paths: IC1 wasted when D1 falters, D2 left un-met when IC2 falters

costs. This is the first such model and will be opensourced as a tool for wider use.

- We identify and model two specific architectural strategies to mitigate volatility-composition and adaptation, and a third architecture-supported strategy-dispersion.
- We present counter-intuitive results based on real-world data about market conditions where these interventions are effective and where they are not, and explore the benefits and drawbacks of combining various interventions.

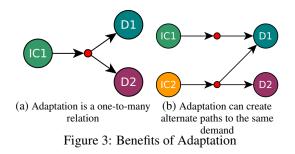
2. **VOLATILITY-RESILIENT CHIP ARCHI-TECTURES**

This section illustrates a problem that may be due to product design and introduces composition and adaptation as two architectural strategies that potentially improve resilience to volatility in supply and demand.

2.1 Fragility of single-demand paths

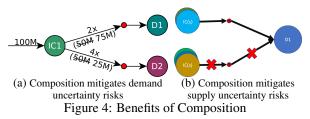
Consider single-demand paths, where one chip can satisfy only one demand; such paths may still occur in spite of the wide prevalence of binning when decisions around binning are made only with the goal of maximizing yield. Figure 2 depicts the clear problems with this approach-when there is only one supply for a demand, the supply input components

(IC1) are wasted when demand (D1) falters, and the demand (D2) is not met when supply input component IC2 falters. The proposed architecture strategies below attempt to spread risk for different supplies and demands across products.



2.2 Adaptation

The adaptation strategy involves architecting and producing a chip that can be modified ("adapted") with low/no overhead, to satisfy multiple types of customer demands. Examples of architectural realizations of adaptation include disabling cores [69] or reducing frequencies to sell a chip at a different performance tier [66]. Multi-ISA designs [86] could also be an architectural realization of adaptation-different demands may make use of the different ISAs the chip supports. Theoretically, adaptation may be able to provide benefits because it delays product differentiation closer to when demand is known. Figure 3 depicts adaptation as a one (chip) to many (demands) mapping. Suppose a firm produces an input component IC1-based saleable product which satisfies demand D1, but can be adapted to also satisfy demand D2 (Figure 3a), and there is a spike in demand for D2 - in that case the firm can adapt a higher proportion of the IC1 it produces to satisfy D2. Additionally, adaptation can create robustness by enabling alternate paths to the same demand (figure 3b) - demand D1 is primarily satisfied by IC1, but if IC2 can be adapted to also be able to satisfy D2, there is redundancy if the supply for D1 if IC1 cuts out. While theoretically adaptation can help mitigate affects of uncertainty, we explore exactly how this may happen in different market volatility situations and the extent of its benefits, in the results section.



2.3 Composition

The composition strategy involves designing and producing sub-components of a chip. These sub-components can then be combined in different quantities to create packaged chips that cater to different demands. Figure 4 depicts composition as a many (sub-components) to one (chip) mapping. An example of a current architectural realization of composition is chiplet-based architectures [78], where chiplets can be interconnected ("composed") using an interposer to make a larger chip. Theoretically, composition can provide benefits when a) there is an intersection in the sub-components used to produce different chips and b) there are multiple (independent) sets of sub-components that can be composed to make chips that satisfy the same demand. As depicted in Figure 4a, (a) helps by allowing the chip firm to decide what packaged chip a sub-component will go toward manufacturing, closer to when it is sold. This is effectively delaying product differentiation much like adaptation. Figure 4b depicts (b) where if the sub-components in path 1 needed to produce the demand goods are suddenly unavailable, the demand can still be satisfied through the sub-components in path 2. Thus helping alleviate the effects of supply volatility. While these theoretical characteristics of composition can potentially provide resilience to uncertainty, a deeper dive in the results section shows a lot more interesting behavior in terms of the ways and extents to which composition can address volatile markets.

3. THE MODEL

To understand how different architectural techniques perform in an uncertain market, we develop a model that computes the profit realized by a chip firm with and without disruptions to demand and supply. We build on the cost model of a generic supply chain developed by Begen et al. [59]; this model can estimate the impact of supply and demand uncertainties on supply chain costs. Crucially, the model was developed in consultation with industry experts at Intel and "a large Korean consumer electronics firm". We augment the model to enable compatibility with commercial stochastic program solvers and the ability to model different computer architecture techniques. Since we are less interested in the absolute numbers produced by the model than the (especially comparative) trends and analyses generated by the model, we believe that this model has sufficient fidelity for the nature of the conclusions we derive (Section 5).

In this paper, we are primarily concerned with the effects of market uncertainty on chip manufacturers. Hence the model we develop optimizes for the highest mean total profit that the firm realizes, given the market conditions. In the results section (Section 5) we use this as our primary metric to assess the effectiveness of the proposed architectural techniques. The high-level equation for profit is given in Eq. (1), with the following sections explaining each of the terms in the equation and how uncertainty is modeled into this equation.

$$P = TC_{ben} - TC_{prod} - TC_{map} - TC_{short}$$
(1)

Table 2 contains a list of parameters we pass to the model. Further, it contains a list of the random and decision variables. Random variables Z^s (Supply Uncertainty) and Z^d (Demand Uncertainty) are used simulate uncertainty in the market. We use these random variables to scale the supplied quantity and amount demanded. Finally, the decision variables are values the model (the firm) can tweak in order to maximize profit.

3.1 Profit Calculation

Table 2: Model Parameters		
Parameters	Description	
\vec{u}_{cost}	Per-Unit Cost	
\vec{y}	Yield Rate	
\vec{n}	NRE Cost	
üben	Per-Unit Benefit	
\vec{u}_{sc}	Shortage Cost	
\vec{b}	Base Demand	
$\vec{\gamma}$ \vec{M}	Mapping Cost	
- M	Mapping Functions	
Random Variables		
\vec{Z}^s	Supply Uncertainty	
\vec{Z}^d	Demand Uncertainty	
Decision Variables		
ō	Ordered	
\vec{q} \vec{U}	Order Quantity	
\vec{U}	Used Mappings	

3.1.1 Cost of Production

To satisfy market demand for chips, a chip firm must first decide what types of chips it puts engineering effort into developing. \vec{o} represents this decision. It is a binary vector where o_i is set to one if and only if the firm decides to design a chip of type 'i'. For each chip of type 'i' that the firm chooses to develop, it incurs a cost n_i for the non-recurring engineering (NRE) costs involved in designing chips of type 'i'.

Once chips are designed, a chip firm must produce them to sell to customers, so the firm decides to order, from a semiconductor foundry, various quantities of the different chips it designed. Let \vec{q} denote this order quantity decision, where q_i represents the number of chips of type 'i' the firm ordered. In our experiments, these can be 16-core, 8-core or 4-core chip(let)s. Note that o_i is zero if and only if q_i is zero; in all other cases, it is one. Once chip order quantities are decided, then based on supply uncertainty, \vec{Z}^s , the firm receives more (or fewer) chips than it ordered. We represent this received quantity as \vec{c}_{recv} and it is the order quantity scaled by supply uncertainty:

$$\vec{c}_{recv} = \vec{q} \circ \vec{Z}^s.$$

After the chips are received, we use \vec{y} , the average yield for each of the chips, to realize what fraction of these received chips is usable. The number of usable chips, \vec{c}_{obt} , is given by

$$\vec{c}_{obt} = \vec{c}_{recv} \circ \vec{y}$$

Each of the chips has a per-unit cost \vec{u}_{cost} . The cost of production incurred by the firm for a particular type of chip is the sum of the unit costs of the received chips, and the non-recurring engineering (NRE) costs incurred in the design. Total production cost for the firm is a sum across all the types of chips:

$$TC_{prod} = \vec{c}_{recv} \cdot \vec{u}_{cost} + \vec{o} \cdot \vec{n}.$$
 (2)

3.1.2 Cost of Mapping

Once the number of chips obtained is known, the firm can decide how to use these chips to maximize profit. The model

takes as input a list of mapping functions \overline{M} . Each mapping function M_j describes a set of inputs M_i^{inp} and outputs M_i^{out} :

$$M_j = (M_i^{inp}, M_i^{out}).$$

Take the baseline case where the firm has a single path from demand to supply in a market for 16-core, 8-core and 4core chips. In this case, we specify three mapping functions.

$$M_1 = (16 \text{ core}, 16 \text{ core})$$

 $M_2 = (8 \text{ core}, 8 \text{ core})$
 $M_3 = (4 \text{ core}, 4 \text{ core}).$

In the case of composition and adaptation, additional mapping functions are added to allow for one-to-many and manyto-one mappings between obtained and sold chips. For example, during adaptation, the firm has the ability to re-purpose 16-core chips as 8-core chips, so mapping function $M_x =$ (16-core, 8-core) is added to the model.

The decision variable \tilde{U} is used to determine how each mapping function is used, where U_j denotes the number of M_j mappings used. The number of used chips \vec{c}_{used} is given by

$$\vec{c}_{used} = \vec{U} \cdot \vec{M^{inp}}$$

Since the number of chips used cannot be more than the number obtained after supply uncertainty and yield losses, the following condition is added to the model

$$c_{used}^i \le c_{obt}^i \; \forall \; i.$$

Using \vec{U} and the mapping output $\vec{M^{out}}$, the model decides the number of chips obtained that can be built for potential sale. Let \vec{c}_{built} be the number of chips the model decides to 'build':

$$\vec{c}_{built} = \vec{U} \cdot M^{out}$$
.

Finally, each mapping function has a cost associated with it. This cost may, for example, represent interposer costs in the case of composition mappings. This cost is an input parameter given by $\vec{\gamma}$, such that γ_j is the mapping cost associated with M_j . Then the total mapping cost incurred by the firm is given by

$$TC_{map} = \vec{U} \cdot \vec{\gamma}. \tag{3}$$

3.1.3 Revenue

For all evaluations, the model simulates a market demand for 16-core, 8-core and 4-core chips. Each of the demanded chips 'i' has a base-demand of value b_i , which is a model parameter. The actual demand for the chips, \vec{c}_{demand} , is given by

$$\vec{c}_{demand} = \vec{b} \circ \vec{Z}^d$$

where Z_i^d is the demand uncertainty for chip of type 'i'. The number of chips sold is determined by the \vec{c}_{demand} and \vec{c}_{built} - the firm cannot sell more chips than it has built and it cannot sell more chips than the amount demanded. The model takes the minimum of the two and uses that as the number of chips sold, \vec{c}_{sold} , given by

$$\vec{c}_{sold} = min(\vec{c}_{demand}, \vec{c}_{built})$$

Chips of type 'i' are each sold at a price u_{ben}^i and hence the total revenue is given by

$$TC_{ben} = \vec{u}_{ben} \cdot \vec{c}_{sold}.$$
 (4)

3.1.4 Shortage Cost

If the firm builds fewer chips than what were demanded, it incurs an additional cost called shortage cost. In addition to the opportunity cost of missed potential sales, there is an additional shortage cost to the firm for each chip it didn't deliver because it didn't satisfy the customer's requirement (concretely, this could be in the form of lost back-orders, lower future order quantities etc.). The per unit shortage cost for a chip 'i' is given by u_{sc}^i and in all the experiments, the shortage cost of chip 'i' is equal to its unit benefit u_{ben}^i . Thus, the total shortage cost incurred by the the firm is

$$TC_{short} = \vec{u}_{sc} \cdot (\vec{c}_{demand} - \vec{c}_{sold}).$$
⁽⁵⁾

In this way, equation (1) represents the net profit the firm makes by combining equations (2), (3), (4) and (5).

3.2 Modeling Uncertainty

For all our experiments, we model supply and demand uncertainties as normal distributions with a mean of one and vary standard deviation to simulate uncertainty. In most experiments, all elements in \vec{Z}^s are sampled from a single normal distribution, i.e, all orders have the same realised surplus or deficit. In some cases, it is a combination of multiple distributions, for example in the case of dispersion. Unlike in the case of supply uncertainty, demand uncertainty, \vec{Z}^d , is unique to each kind of chip and this is consistent across all experiments. For each evaluation, we sample multiple values for \vec{Z}^d and \vec{Z}^s . Then the decision variables are optimized to maximize $\mathbb{E}[P]$ given by

$$\mathbb{E}[P] = \mathbb{E}[TC_{ben}] - \mathbb{E}[TC_{prod}] - \mathbb{E}[TC_{map}] - \mathbb{E}[TC_{short}].$$

4. METHODOLOGY

4.1 Simulation Framework

To accurately assess the impact of architectural techniques on supply and demand uncertainty, we identify *optimal* chip order ($\vec{q} & \vec{o}$) and chip mapping policies (\vec{U}) that maximize expected total profit, $\mathbb{E}[P]$. This is done so results under different interventions and market conditions are comparable. Note that some decision variables need to be determined independent of some random variables. For example, \vec{q} and \vec{o} need to be decided before demand and supply are known. Also Z^d and Z^s are probability distributions, not numeric parameters. So, in some cases, a single decision is made for all the samples in the distribution (exception to this are detailed later).

Table 3: Staging for Decision Variables and Recourse Parameters

Decision Variable	Decision Stage
q	1
0	1
U	2 or 3
Random Variable	Recourse Stage
Z^s	2
Z^d	2 or 3

Since this optimization problem is a stochastic mixedinteger non-linear program, we use a multistage stochastic program with recourse (MSPR) [64, 80] to solve it. The idea of an MSPR is to sequence the decision variables across multiple temporal stages, with latter stages having 'recourse' to information that was not available in earlier stages. We perform simulations for different permutations of decisionmaking stages and recourse. These are summarized in Table 3. The value of order quantity \vec{q} and decision to order \vec{o} are always determined in stage 1, without recourse to supply and demand. Mapping \vec{U} is determined either in stage 2 once supply is known or in stage 3 once both demand and supply are known. We offer this flexibility for mapping since, for example, the decision to sell a 16-core processor as an 8-core processor can be made very late in the production process, since disabling cores can be done via firmware. So this can be done once demand is realized.

We use the global solver in the commercial mathematical programming software Lingo [75] for finding globally optimal chip orders and mappings. We generate Lingo models using a Rust library which transforms collections of produced goods, demanded goods, mappings, and additional constraints into Lingo models.

4.2 Model Parameters

For a study like this, it is critical that model parameters represent reality. Publicly available data [6, 7, 56, 73] suggests that most fluctuations of semiconductor products remain within $\pm 50\%$ of expectation. Additionally, our analysis of semiconductor revenue over the past 30 years (Figure 1c) shows up to a 15% standard deviation in profit over a 10 year period. Our analysis of the PC market over the last four years (Figure 1a) shows a 27% standard deviation in the quantity of PCs shipped, and that individual vendors around a 11% higher standard deviation in profit with respect to the industry, or about a 30% standard deviation. As such, our experiments are run between the standard deviation values for the semiconductor industry and for individual vendors in the PC industry, with some additional tests on either side of that range.

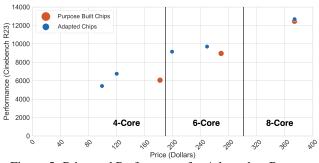


Figure 5: Price and Performance for Adapted vs Purpose Built Chips

We performed several case studies centered around 22 nm 16, 8, and 4-core dies. To estimate their area, we scaled the area for a 32-core monolithic-chip from [70] based on the number of cores and then scaled it further to 22 nm. To estimate the yield, \vec{y} , of these dies, we used the model in a previous study from AMD [72] (*Yield* = $(1 + (D_0/n \cdot A_{crit})/\alpha)^{-\alpha})$ assuming a 300 mm wafer, 12 metal layers, $Frac_{crit}$ (wire) of 0.2625, $Frac_{crit}$ (logic) of 0.75, and clustering factor α of 1.

To estimate the unit cost, \vec{u}_{cost} , of each of the dies, we first estimate the recurring engineering (RE) cost for a given order as $RE_cost = (order/N_{dies}) * RE_wafer_cost_logic$, where $RE_wafer_cost_logic = 24300$ was taken from [79] (Table IV). The cost estimates (normalized w.r.t 45 nm wafer cost) are largely based on published industrial data. To estimate the NRE cost, \vec{n} , we again used cost numbers from [79] (Table IV). NRE cost has two parts, 1) area dependent NRE design cost and 2) area independent NRE mask set cost. We scale the design cost with our core area and add one mask set cost per design to estimate the total NRE cost. Interposer costs are derived the same way using numbers from [79] (Table IV). For validation, we calculated the cost of a 32core monolithic chip and 32-core chip composed of 4 8-core chiplets through our model. We found the 32-core chip built from composing 8-core chiplets was 1.71× cheaper. For comparison, [70] claims that their 32-core chip composed using 4x8-core chiplets is $1.69 \times$ cheaper to build than their 32-core monolithic-chip.

For each demanded chip, we assume a base demand (\vec{b}) of 10⁸ chips. Unit benefits were computed as follows $u_{ben} = 2\left(u_{cost} + \frac{n}{b}\right)$ (assuming a 50% gross margin [51,55]). Due to the importance of time-to-market, unit shortage cost, \vec{u}_{sc} , is equal to unit benefit \vec{u}_{ben} .

We use mapping functions (\overline{M}) and the mapping decision variable (\overline{U}) to realize our architectural techniques - composition and adaptation. Each mapping M_j has a mapping cost γ_j associated with it. For composition, we primarily explore monolithic chip vs chiplet based designs. Chiplet based designs have a variable interposer costs. Hence, for any mapping that performs composition, γ takes a value for an interposer of area equal to the overall produced chip area.

Our adaptation technique assumes mappings from larger chips to smaller chips. For example, a 16-core or 8-core chip can be rebadged and sold as an 8-core or 4-core chip, given market constraints. To estimate adaptation cost, we looked at real world data. Figure 5 shows the relationship of inflation-adjusted price and performance for purpose-built vs adapted chips for several consumer chips from Intel and AMD. Prices for chips with the same core count are roughly the same and also show similar performance (Cinebench score [2, 3, 23, 24, 25]). Hence, we allow unit benefit of adapted chips to be the same as monolithic chips built for that purpose and the mapping cost of this intervention is zero.

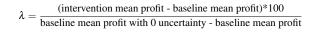
4.3 **Experiment Configurations**

In Section 5, we simulate a market for 4-core, 8-core, and 16-core chips using the model and methodology above. Unless otherwise mentioned, we use a single normal distribution to sample supply uncertainty for all the chips, \vec{Z}^s , and three independent normal distributions to sample demand uncertainty for each of the chips, \vec{Z}^d . For our baseline case, we study a rigid one-to-one mapping between produced goods and demanded goods.

For our architectural intervention techniques, composition and adaptation, we introduce new mappings. In composition, we allow the firm to produce 16-core chips using two 8-core, four 4-core, and a combination of 8-core and 4-core chiplets. We also add a mapping to allow 8-core chips to be built using two 4-core chiplets. The mapping cost (interposer cost) for the composed 8-core chip is half that of a composed 16-core chip since the size of the interposer used is halved. In adaptation, we allow manufactured 16-core chips to be repurposed and sold as 8-core or 4-core chips, and manufactured 8-core chips to be sold as 4-core chips.

5. **RESULTS**

Here we present results on the mean and standard deviation of total profit under several regimes of uncertainty to understand the benefits and drawbacks of different architectural interventions. The mean profit for the firm is calculated across various samples of demand and supply. And the standard deviation in profit highlights the variance in profit seen across these samples. We present results as line plots, separately for mean profit and for standard deviation of expected profits. To isolate and better and understand the benefits of the various architectural interventions, we simulate market conditions with uncertainty only in the supply of goods, only in the demand for goods, and with both. We study the mappings used by the optimizer to justify the benefits and drawbacks of the various experiments. An important metric that we consider is lambda (λ), which is the percentage of the loss incurred due to uncertainty that is made up by the intervention. Lambda is calculated as



5.0.1 Fragility of single-demand paths

Our baseline results demonstrate the fragility of having only a single path between produced and demanded chips.

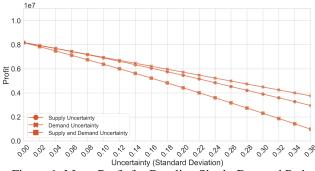
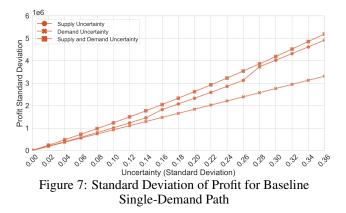


Figure 6: Mean Profit for Baseline Single-Demand Path



The baseline models supply and demand for three different chips (4, 8 and 16-core) and each chip is sold exactly as it is produced.

The results for the baseline (Figure 6) show that at the highest level of uncertainty (standard deviation of 0.36) the firm sees a 93%, 63%, and 54% drop in mean profit for supply+demand uncertainty, supply uncertainty and demand uncertainty, respectively. This drop is attributable to the in-ability of the supply chain to cope with uncertainty. Not only is there a significant decrease in average profit, there is also an increase in the range of profits the company could see (Figure 7). At high levels of variance, the standard deviation of profit is almost equal to the mean profit when there is no variance. Clearly, this level of reduced expected profit and variance in profit is undesirable.

5.1 Architectural Strategies

5.1.1 Composition

As discussed earlier, composition can enhance resilience by enabling multiple paths to produce the same good. Like the baseline, there are 4, 8, and 16-core goods produced, but here these can be composed in any way possible to make 4, 8, and 16-core packaged chips. An added interposer cost is incurred for every chip produced using composition. Here we use the three-stage model where supply uncertainty is realized first, and then demand uncertainty is resolved after chips are mapped (packaged) using \vec{U} . These assumptions and

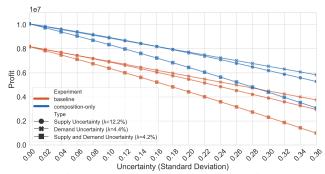


Figure 8: Mean Profit with Composition

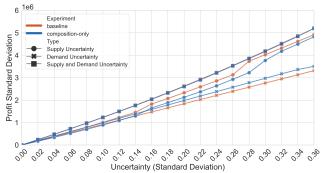


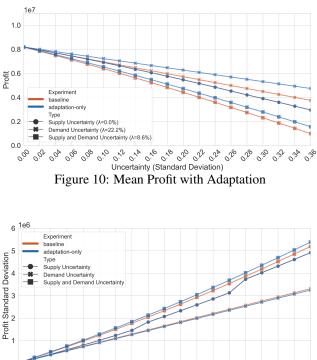
Figure 9: Standard Deviation of Profit with Composition

configurations are made keeping in mind how composition is carried out currently - chiplets are sourced from the same supplier, and chiplet-based chips end up being sold several months after they are packaged, so demand is not known at the time of packaging.

Results for mean profit using composition are presented in Figure 8. We see that mean profit is 25% higher with composition compared to the baseline when there is no variance. All of this improvement is attributable to yield benefits of composition, however. Indeed, an analysis of mappings used (\vec{U}) , shows that when there is no uncertainty in the market, the model orders only 4-core chips and makes 16-core and 8core chips out of these. After factoring out the improvement due to yield benefit, we see λ is only 12.2% when there is supply uncertainty, 4.2% when there is supply and demand uncertainty, and 4.4% when there is only demand uncertainty. The absolute increase in profit (between composition and baseline) remains almost steady when demand uncertainty is present across the range of uncertainties studied. There is a small improvement when there is only supply uncertainty because the limited supply of chips under supply constraints is directed to satisfy the demand with the greatest profit (the 16-core part). This suggests that composition by itself hardly improves profits any more than it does at 0 uncertainty.

Results of profit variance using composition are presented in Figure 9. The results show that **composition does have benefits to profit variance**, especially in regimes of exclusive supply uncertainty. Profit-variance reduction is negligible in markets with only demand uncertainty.

5.1.2 Adaptation





As seen earlier, adaptation can provide benefits to resilience by allowing multiple types of demand to be satisfied by a single good. To understand the benefits of adaptation, we model it as it is practiced in the industry, in the form of disabling cores and selling as a lower-end product. Because fusing (to disable cores) can be done any time after the part is packaged, we assume that the level of demand is known at the time adaptation is carried out.

Figure 10 shows the benefits of adaptation over the baseline. Unlike composition, there are no inherent benefits to profit with adaptation, so the profit when there is no variance is the same as the baseline case. For adaptation, we see λ is 22.2% when there is demand uncertainty, 8.6% when there is supply and demand uncertainty, and 0.0% when there is only supply uncertainty. This shows that while adaptation provides a considerable benefit when only demand uncertainty is present, there is disproportionately less benefit when both supply and demand uncertainties are present. This is because adaptation is limited in its ability to redistribute produced goods to demanded goods when there is limited supply (eg. a supply of 4-core can only satisfy 4-core demand, and a demand of 16-core can only be satisfied by a 16-core supply). There is no benefit from adaptation when there is only supply uncertainty.

Results for standard deviation of profit are presented in Figure 11. Adaptation largely preserves the baseline standard deviation when there is demand uncertainty while providing significantly improved profits, signaling an improvement in variance of profit. There is negligible improvement in variance under the other two conditions. 5.1.3 Fixing Composition's problems through justin-time packaging

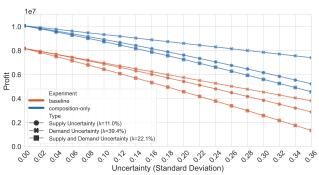
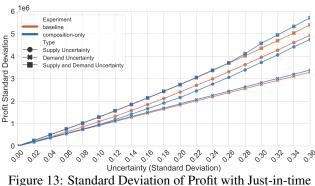


Figure 12: Mean Profit with Just-in-time Packaging



Packaging

In the previous analysis, it is seen that composition does not increase mean profit when demand uncertainty is present because it is unable to redistribute the received chiplets to meet variance in demand. This is because demand is unknown when the composition mapping takes place. So we ask, what may change if the level of demand is known at the time of packaging, perhaps through a breakthrough in distributed packaging at last-mile locations? This analysis is run the same as the composition analysis, except now the model knows demand while choosing the composition mappings.

Figure 12 shows results for mean profit under composition with just-in-time packaging. When factoring out the yield savings, we calculate a lambda of 39.4% with demand uncertainty, 11.0% with supply uncertainty and 22.1% with both supply and demand uncertainty, showing **just-in-time composition significantly mitigates losses**. This is a significant improvement over baseline composition where there was virtually no benefit when demand uncertainty was present.

Figure 13 shows standard deviation of profit with justin-time composition. Profit-normalized variance is reduced in all cases, albeit to different degrees. This configuration maintains similar variance under demand and supply+demand uncertainty while increasing profit, and has a lower variance under supply uncertainty while providing greater profits.

5.1.4 Enhancing Adaptation with Composition

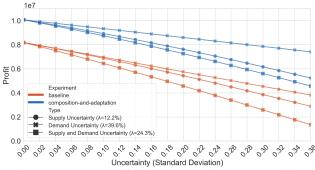


Figure 14: Mean Profit with Adaptation + Composition

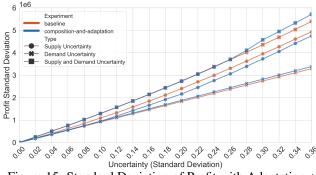


Figure 15: Standard Deviation of Profit with Adaptation + Composition

Adaptation has limited efficacy when supply uncertainty is introduced (exclusively or in concert with demand uncertainty). But composition seems to provide some benefits here, so we explore if adding composition to adaptation may improve on baseline adaptation. We model composition with adaptation in the following way: It is the same as composition as described in the previous section, except after chips are packaged, it is still possible to disable cores and repurpose chips to meet multiple demands.

Figure 14 shows mean profits using adaptation+composition. We see λ is 39.6% with demand uncertainty, 24.3% with supply and demand uncertainty, and 12.2% with supply uncertainty. These numbers represent an all-round improvement over baseline adaptation and, in fact, even an improvement over just-in-time composition. Compared to just-in-time composition, λ is 10 points higher in adaptation + composition for cases with only demand uncertainty and supply+demand uncertainty. Improvements to supply resilience are similar to the previous section, but there are improvements to demand resilience with this technique because the relation between the produced products and demanded products is fully connected. Eg. with baseline adaptation, a produced 8-core chip could be sold as either a 4 or 8-core chip. Now it can be composed to make a 16-core chip, sold as an 8-core, or adapted as a 4-core.

Figure 15 shows standard deviation of profit with adaptation and composition. This configuration maintains similar variance in profit under demand and supply+demand uncertainty while increasing profit, and lower variance in supply uncertainty while providing greater profits.

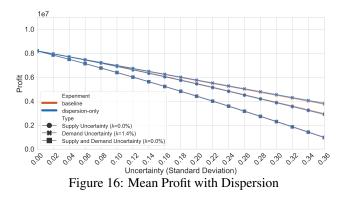
While adding composition to adaptation improves adapta-

tion significantly, we continue to see minimal improvement to profit in the regime of supply uncertainty, and negligible reduction in profit variance. To address these drawbacks, we rely on *dispersion*.

5.2 Dispersion

Dispersion can improve resiliency by dispersing the production across suppliers who have a degree of independence with respect to the factors affecting their production. While dispersion is not strictly an architectural strategy, it can be enabled by architectural changes. For example, aggregating low-speed components in a design (eg. I/O) onto a separate chiplet will allow it to be manufactured on a trailing edge process (and therefore by more foundries), in effect 'dispersing' a portion of the overall design. There are two ways in which we model dispersion. In the first way, each of the produced chips is supplied from a different supplier. In the second way (dispersion2), we introduce two suppliers, and each produced chip is sourced from these two suppliers. We look at each of these two configurations separately.

5.2.1 Using unique suppliers for each product



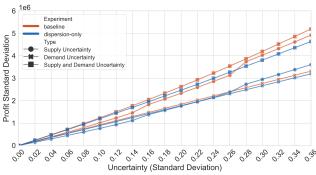


Figure 17: Standard Deviation of Profit with Dispersion

Using unique suppliers for each product decouples the supplies of each of the products from each other, potentially offering improved resiliency against swings in supply. Concretely, this means three different supply distributions are used to sample each value in \vec{Z}^s corresponding to each produced chip.

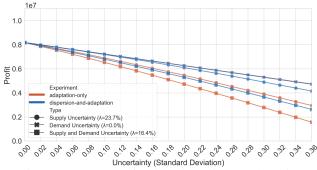
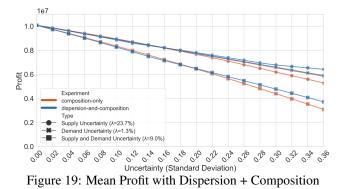


Figure 18: Mean Profit with Dispersion + Adaptation



Profit results for dispersion alone are presented in Figure 16. We notice **this provides no benefit over the baseline for profit**. This is because surpluses in the supply of one product cannot make up for deficits in the supply of the other since chips cannot be transformed in the baseline case. However, when transformations between produced and demanded goods are enabled by adding dispersion to adaptation (Figure 18) or to composition (Figure 19) we see significant results. We compare these to baseline composition and adaptation. Adding dispersion to composition produces a λ of 23.7% and 9.0% in supply and supply + demand uncertain markets, respectively. For adaptation with dispersion, these values are 23.7% and 16.4% respectively.

Even though there were no improvements to profit from dispersion only, there was a **36.9% reduction in profit standard deviation under supply uncertainty** and a 20.7% reduction in profit standard deviation under supply + demand uncertainty (Figure 17). A similar improvement is seen for dispersion profit standard deviation when used with composition and adaptation.

5.2.2 Using two suppliers across each product

Under this strategy, there are two independent suppliers and all products in the market can be sourced from either one of them. Concretely, this translates to two independent supply distributions each operating on half the goods ordered. The two suppliers are equal in all ways, so we force the model to order an equal quantity from both of them.

Baseline results depicted in Figure 20 show that a multisourcing strategy has benefits even without other architectural interventions. **Results show an impressive 49.3%** λ in **supply-uncertain markets and a 24.0%** λ for supply + de-

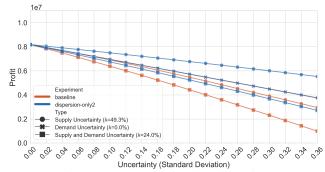


Figure 20: Mean Profit with Dispersion2

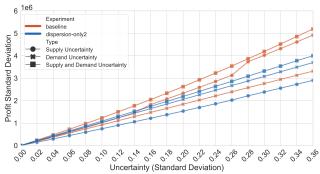


Figure 21: Standard Deviation of Profit with Dispersion2

mand. Significant improvements are seen in profit variance as well. Multi-sourcing dispersion used with composition (Figure 23) or adaptation (Figure 22) continues to provide improvements over their respective baselines. For dispersion + adaptation we see a λ of 49.3% and 29.3% under supply and supply+demand uncertainty, respectively, and for dispersion +composition these numbers are 50.4% and 24.4%, respectively. These improvements are much greater than that seen with the previous dispersion technique, yet similar to the baseline case of multi-supplier dispersion, suggesting that composition and adaptation do not further enhance multi-sourcing dispersion under supply uncertainty.

For standard deviation in profit, we see a reduction between 25% to 40.8% under the multi-sourcing strategy in the baseline (Figure 21), and these improvements largely stay the same when composition and adaptation are added.

5.3 Market Mechanisms

Till now, our experiments have modeled a market for semiconductor chips statically without any consideration for market mechanisms. But what happens when market mechanisms like competition and/or elasticity of demand are considered? Would the efficacy of any of the techniques we proposed previously change? To explore this, we introduce a partial equilibrium-based [68] model of market dynamics to our model; partial equilibrium can model the first-order effects of competition, production and demand. This is incorporated into our model by integrating a demand curve. With this addition, the unit benefit that a good is sold at is linearly related to number of goods purchased. The new unit benefit is given by:

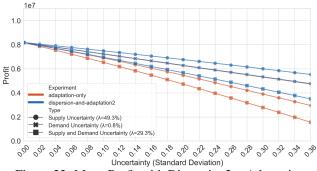


Figure 22: Mean Profit with Dispersion2 + Adaptation

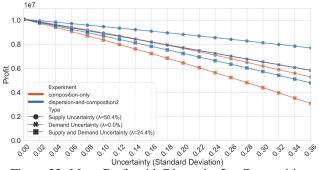


Figure 23: Mean Profit with Dispersion2 + Composition

Unit Benefit = (Goods Sold - Base Demand)* Market Elasticity + Base Price

where base demand is the same as it was in the baseline case, and base price is the same as unit benefit in the previous model. Market elasticity signifies how elastic the demand is (i.e the extent to which demand changes with a change in price). To ensure comparability between the two models, the parameter values for elasticity for each of the different chips were chosen such that the optimal point in the demand curve is at the point where base demand number of chips are sold, each at base price, yielding the same revenue as in the previous model. Combining HP's PC sales quantities from figure 1a and the corresponding revenue data from HP [32,33,34,35,36,37,38,39,40,41,42,43,44,45,46,47,53,54], and assuming chips comprise 10% of the cost of a PC, we see a similar magnitude of elasticity to our calculated parameter values. Demand uncertainty is modeled through shifting the demand curve left or right by scaling base demand by the demand factor. The supply side of the model remains unchanged. To ensure consistency between the models, shortage cost is also modeled in the same way. The model is aware of the market mechanism and optimizes for highest expected profit accordingly.

Results on the effect of considering market mechanisms are presented in Figure 24. Results are presented for supply + demand uncertainty case. Despite the differing shapes (linear - market mechanism-agnostic vs negative exponential market-mechanism aware), the two curves are relatively close to each other and come closer as uncertainty increases. The calculated λ value (when considering knowledge of uncer-

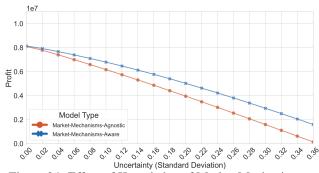


Figure 24: Effect of Knowledge of Market Mechanism on Mean Profit

tainty as an intervention) is 30% at low values of uncertainty, and this reduces to 18.8% at the high end of uncertainty variance. This suggests that **knowledge of market mechanisms is largely equivalent to adding a low-performing intervention in terms of profit benefits**. As we have shown, interventions can be composed to improve results, and the reason knowledge of market mechanisms is making a loss is the same as in the baseline model, except losses are padded by increased selling prices (due to awareness of the market) when supply is low or demand is high. Given this, we conclude that all our previous conclusions continue to be interesting and valid when market mechanisms are considered.

6. SUMMARY AND CONCLUSIONS

We presented the first study on the role of chip architecture in improving semiconductor supply chain resilience. We first found that one-to-one chip-to-demand mapping, as is common today, is a significant source of fragility, leading to over a three-fourth reduction in profits at our estimates of industry supply and demand uncertainty. As currently done, the architectural strategy of *composition* is ineffective in improving profits beyond the yield savings it offers, but justin-time composition mitigates up to 33% of the losses from demand volatility. This also mitigates some supply volatility by directing (limited) supply to the highest margin product. The strategy of *adaptation* can mitigate 20% of losses from demand uncertainty but is unable to mitigate losses from supply volatility. Moreover, adding composition on top of adaptation can double its effectiveness in demand-volatile situations and provide some benefits in supply-volatile situations. In developing two flavors of dispersion, we showed that multi-sourcing dispersion is superior, mitigating nearly half of the losses from supply uncertainty. Finally, we showed that knowledge of how the market behaves helps improve profits but less than most of our proposed interventions, and in fact these interventions continue to provide benefits when market mechanisms are considered. Overall, nearly half of the losses from both supply and demand volatility can be mitigated using the techniques we propose.

REFERENCES

- "After three consecutive years of growth, idc forecasts worldwide semiconductor revenue to decline by 7.2% in 2019." [Online]. Available: https://web.archive.org/web/20221122031546/https: //www.bloomberg.com/press-releases/2019-05-15/after-threeconsecutive-years-of-growth-idc-forecasts-worldwidesemiconductor-revenue-to-decline-by-7-2-in-2019
- [2] "Amd ryzen 3 3300x benchmark, test and specs." [Online]. Available: https://www.cpu-monkey.com/en/cpu-amd_ryzen_3_3300x
- [3] "Amd ryzen 5 3600 vs amd ryzen 5 3600x benchmark, comparison and differences." [Online]. Available: https://www.cpu-monkey.com/en/compare_cpu-amd_ryzen_5_3600vs-amd_ryzen_5_3600x
- [4] "Asahi kasei semiconductor factory fire resumption time time substitute production request to other companies." [Online]. Available: https://www.tellerreport.com/business/2021-02-09-%0A---asahikasei-semiconductor-factory-fire-resumption-time-time-substituteproduction-request-to-other-companies-%0A--.Hk8_mw31Wu.html
- [5] "Capacity utilization: Manufacturing: Durable goods: Semiconductor and other electronic component (naics = 3344)." [Online]. Available: https://www.federalreserve.gov/releases/g17/Revisions/20220628/ ipdisk/alltables.txt
- [6] "Coronavirus: Implications for semiconductor demand | McKinsey." [Online]. Available: https://www.mckinsey.com/industries/semiconductors/ourinsights/coronavirus-implications-for-the-semiconductor-industry
- [7] "Gartner Says Global Smartphone Sales Declined 20% in Second Quarter of 2020." [Online]. Available: https://www.gartner.com/en/newsroom/press-releases/2020-08-25gartner-says-global-smartphone-sales-declined-20--in-
- [8] "Gartner says worldwide pc shipments declined 12.3 percent in the first quarter of 2020 due to coronavirus pandemic," *Gartner*. [Online]. Available: https://www.gartner.com/en/newsroom/press-releases/2020-04-13gartner-says-worldwide-pc-shipments-declined-12-point-3-percentin-the-first-quarter-of-2020-due-to-coronavirus-pandemic
- [9] "Gartner says worldwide pc shipments declined 4.6 percent," *Gartner*.
 [Online]. Available: https://www.gartner.com/en/newsroom/press-releases/2019-04-10-gartner-says-worldwide-pc-shipments-declined-4-6-perc
- [10] "Gartner says worldwide pc shipments declined 5 percent in fourth quarter of 2021 but grew nearly 10 percent for the year," *Gartner*. [Online]. Available: https://www.gartner.com/en/newsroom/press-releases/2022-01-12gartner-says-worldwide-pc-shipments-declined-5-percent-in-fourthquarter-of-2021-but-grew-nearly-10-percent-for-the-year
- [11] "Gartner says worldwide pc shipments declined 7 percent in first quarter of 2022," *Gartner*. [Online]. Available: https://www.gartner.com/en/newsroom/press-releases/2022-04-11gartner-says-worldwide-pc-shipments-declined-7-percent-in-firstquarter-of-2022
- [12] "Gartner says worldwide pc shipments experienced the sharpest decline in nine years in second quarter of 2022," *Gartner*. [Online]. Available: https://www.gartner.com/en/newsroom/press-releases/2022-07-11gartner-says-worldwide-pc-shipments-experienced-the-sharpestdecline-in-nine-years-in-second-quarter-of-2022
- [13] "Gartner says worldwide pc shipments grew 1 percent in third quarter of 2021," Gartner. [Online]. Available: https://www.gartner.com/en/newsroom/press-releases/2021-10-11gartner-says-worldwide-pc-shipments-grew-1-percent-in-thirdquarter-of-2021
- [14] "Gartner says worldwide pc shipments grew 10.7 percent in the fourth quarter of 2020 and 4 point 8 percent for the year," *Gartner*. [Online]. Available: https://www.gartner.com/en/newsroom/press-releases/2021-01-11gartner-says-worldwide-pc-shipments-grew-10-point-7-percent-inthe-fourth-quarter-of-2020-and-4-point-8-percent-for-the-year
- [15] "Gartner says worldwide pc shipments grew 1.1 percent in third quarter of 2019," *Gartner*. [Online]. Available: https://www.gartner.com/en/newsroom/press-releases/2019-10-10gartner-says-worldwide-pc-shipments-grew-1point1-percent-in-

third-quarter-of-2019

- [16] "Gartner says worldwide pc shipments grew 1.5 percent in second quarter of 2019," *Gartner*. [Online]. Available: https://www.gartner.com/en/newsroom/press-releases/2019-07-11gartner-says-worldwide-pc-shipments-grew-1point5percent-insecond-quarter-of-2019
- [17] "Gartner says worldwide pc shipments grew 2.3 percent in 4q19 and .6 percent for the year," *Gartner*. [Online]. Available: https://www.gartner.com/en/newsroom/press-releases/2020-01-13-gartner-says-worldwide-pc-shipments-grew-2-point-3-percent-in-4q19-and-point-6-percent-for-the-year
- [18] "Gartner says worldwide pc shipments grew 2.8 percent in second quarter of 2020," *Gartner*. [Online]. Available: https://www.gartner.com/en/newsroom/press-releases/2020-07-09gartner-says-worldwide-pc-shipments-grew-2point8-percent-insecond-quarter-of-2020
- [19] "Gartner says worldwide pc shipments grew 32 percent in first quarter of 2021," *Gartner*. [Online]. Available: https://www.gartner.com/en/newsroom/press-releases/2021-04-12gartner-says-worldwide-pc-shipments-grew-32-percent-in-firstquarter-of-2021
- [20] "Gartner says worldwide pc shipments grew 3.6 percent in the third quarter of 2020," *Gartner*. [Online]. Available: https://www.gartner.com/en/newsroom/press-releases/2020-10-12gartner-says-worldwide-pc-shipments-grew-3-point-six-percent-inthe-third-quarter-of-2020
- [21] "Gartner says worldwide pc shipments grew 4 point six in second quarter of 2021," *Gartner*. [Online]. Available: https://www.gartner.com/en/newsroom/press-releases/2021-07-12gartner-says-worldwide-pc-shipments-grew-4-point-six-in-secondquarter-of-2021
- [22] "Historical billings report." [Online]. Available: https://www.wsts.org/esraCMS/extension/media/f/WST/5711/ WSTS-Historical-Billings-Report-Sep2022data.xlsx
- [23] "Intel core i7-7700k vs amd ryzen 3 3100 benchmark, comparison and differences." [Online]. Available: https://www.cpu-monkey.com/en/compare_cpuintel_core_i7_7700k-vs-amd_ryzen_3_3100
- [24] "Intel core i7-8700k benchmark, test and specs." [Online]. Available: https://www.cpu-monkey.com/en/cpu-intel_core_i7_8700k
- [25] "Intel core i9-9900k vs intel core i7-10700k benchmark, comparison and differences." [Online]. Available: https://www.cpu-monkey.com/en/compare_cpuintel_core_i9_9900k-vs-intel_core_i7_10700k
- [26] "Market growth rate peaks after a strong 2017; idc forecasts semiconductor revenue growth of 7.7%, reaching \$450 billion in 2018."
 [Online]. Available: https://web.archive.org/web/20221122031651/https: //www.bloomberg.com/press-releases/2018-05-21/market-growthrate-peaks-after-a-strong-2017-idc-forecasts-semiconductorrevenue-growth-of-7-7-reaching-450-billion-in-2018
- [27] "Semiconductor downturn to continue into 2020 due to covid-19 impact; idc forecasts worldwide non-memory semiconductor revenue to decline 7.2% in 2020." [Online]. Available: https://web.archive.org/web/20210912143858/https: //www.businesswire.com/news/home/20200504005178/en/ Semiconductor-Downturn-to-Continue-Into-2020-Due-to-COVID-19-Impact-IDC-Forecasts-Worldwide-Non-Memory-Semiconductor-Revenue-to-Decline-7.2-in-2020
- [28] "Semiconductor forecast database, worldwide, 2q18 update," *Gartner*. [Online]. Available: https://www.gartner.com/en/documents/3881084
- [29] "Semiconductor forecast database, worldwide, 2q19 update," Gartner. [Online]. Available: https://www.gartner.com/en/documents/3942113
- [30] "Semiconductor forecast database, worldwide, 2q20 update," Gartner. [Online]. Available: https://www.gartner.com/en/documents/3986978
- [31] "Semiconductor forecast database, worldwide, 2q21 update," *Gartner*. [Online]. Available: https://www.gartner.com/en/documents/4003111
- [32] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2018. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_financials/2018/q1/q1-2018-quarterly-results.pdf

- [33] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2018. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_financials/2018/q2/q2-2018-quarterly-results.pdf
- [34] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2018. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_financials/2018/q3/q3-2018-earnings-webtables.pdf
- [35] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2018. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_financials/2018/q4/q4-2018-earnings-webtables.pdf
- [36] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2019. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_financials/2019/Q1/Q119-HP-Inc-Quarterly-Results.pdf
- [37] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2019. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_financials/2019/Q2/Q219-HP-Inc.-Quarterly-Results-Web-Tables.pdf
- [38] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2019. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_financials/2019/q3/q3-2019-earnings-webtables.pdf
- [39] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2019. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_financials/2020/q1/Q120-HP-Inc.-Earnings-Press-Release.pdf
- [40] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2020. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_financials/2020/q1/q1-2020-quarterly-results.pdf
- [41] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2020. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_financials/2020/q2/q2-2020-quarterly-results.pdf
- [42] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2020. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_financials/2020/q3/Q320-HP-Inc-Earnings-Press-Release.pdf
- [43] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2020. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_events/2020/Q42020/ Q4FY20-HP-Inc.-Press-Release-vFINAL.pdf
- [44] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2021. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_events/2021/Q1/Q1FY21-Press-Release-FINAL.pdf
- [45] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2021. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_events/2021/Q2/Q2FY21-HP-Inc.-Press-Release.pdf
- [46] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2021. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_events/2021/Q3/Q3FY21-HP-Inc.-Press-Release.pdf
- [47] "Hp inc., and subsidiaries consolidated condensed statements of earnings," 2021. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_financials/2021/q4/Q421-HP-Inc.-Press-Release.pdf
- [48] "Semiconductor factory fire in japan adds to global shortage," Jun 2021. [Online]. Available: https://autovista24.autovistagroup.com/news/semiconductor-factoryfire-japan-adds-global-shortage/
- [49] "Semiconductor shortages causing automakers to cut production for early 2021," Jan 2021. [Online]. Available: https://info.fusionww.com/blog/semiconductor-shortages-causingautomakers-to-cut-production-for-early-2021
- [50] "Tsmc (tsm) revenue," Oct 2021. [Online]. Available: https://companiesmarketcap.com/tsmc/revenue/
- [51] "Amd gross margin 2010-2022: Amd," Sep 2022. [Online]. Available:

https://web.archive.org/web/20221122070139/https: //www.macrotrends.net/stocks/charts/AMD/amd/gross-margin

- [52] "Did the computer chip shortage affect inflation?" May 2022. [Online]. Available: https://www.stlouisfed.org/on-theeconomy/2022/may/did-computer-chip-shortage-affect-inflation
- [53] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2022. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_financials/2022/q1/ Q1FY22-HP-Inc.-Press-Release.pdf
- [54] "Hp inc.. and subsidiaries consolidated condensed statements of earnings," 2022. [Online]. Available: https://s2.q4cdn.com/602190090/files/doc_financials/2022/q2/ Q2FY22-HP-Inc.-Press-Release.pdf
- [55] "Intel profit margin 2010-2022: Intc," Sep 2022. [Online]. Available: https://www.macrotrends.net/stocks/charts/INTC/intel/profitmargins
- [56] "Results from Semiconductor Supply Chain Request for Information," Jan. 2022. [Online]. Available: https://www.commerce.gov/news/blog/2022/01/resultssemiconductor-supply-chain-request-information
- [57] E. Barrett, "Semiconductors are a us-china trade war weapon. can tsmc serve both sides?" Aug 2020. [Online]. Available: https://fortune.com/2020/08/10/us-china-trade-war-semiconductorschips-tsmc-chipmakers/
- [58] H. Bauer, O. Burkacky, P. Kenevan, S. Lingemann, K. Pototzky, and B. Wiseman, "Semiconductor design and manufacturing: Achieving leading-edge capabilities," Aug 2020. [Online]. Available: https://www.mckinsey.com/industries/advanced-electronics/ourinsights/semiconductor-design-and-manufacturing-achievingleading-edge-capabilities
- [59] M. A. Begen, H. Pun, and X. Yan, "Supply and demand uncertainty reduction efforts and cost comparison," *International Journal of Production Economics*, vol. 180, pp. 125–134, 2016. [Online]. Available: https: //www.sciencedirect.com/science/article/pii/S0925527316301591
- [60] R. Beggin, "As tensions simmer over taiwan, global chip supply hangs in the balance," Nov 2021. [Online]. Available: https: //www.proquest.com/wire-feeds/as-tensions-simmer-over-taiwanglobal-chip-supply/docview/2591857351/se-2?accountid=14553
- [61] D. Bonderud, "How supply chain woes are affecting pc vendors and what's next for the industry." [Online]. Available: https://www.spiceworks.com/tech/hardware/articles/supply-chainimpacting-pc-vendors/
- [62] K. Carlson, "Nxp could lose \$100 million due to weather shutdown of austin plants," Mar 2021. [Online]. Available: https://www.statesman.com/story/business/2021/03/12/nxp-couldlose-100-million-due-weather-shutdown-austin-plants/4664621001/
- [63] K. Carlson, "Shutdown of austin fab during freeze cost samsung at least \$268 million," Apr 2021. [Online]. Available: https://www.statesman.com/story/business/2021/04/30/austin-fabshutdown-during-texas-freeze-cost-samsung-millions/4891405001/
- [64] C. Condevaux-Lanloy and E. Fragniere, "Setstoch: A tool for multistage stochastic programming with recourse," in *Logilab*, *University of Geneva*. Citeseer, 1998.
- [65] S. Endicott, "Semiconductor revenue expected to reach \$522 billion this year." [Online]. Available: https://www.windowscentral.com/semiconductor-revenue-expectedreach-522-year
- [66] N. Evanson, "Explainer: What is chip binning? hitting the silicon lottery jackpot." [Online]. Available: https://www.techspot.com/article/2039-chip-binning/
- [67] R. Fowler, "Drought and fire activity: What's climate change got to do with it?" Aug 2016. [Online]. Available: https://lamont.columbia.edu/news/drought-and-fire-activity-whatsclimate-change-got-do-it
- [68] J. Gilbert, "Partial equilibrium analysis part i a basic partial equilibrium ... - escap," May 2017. [Online]. Available: https://unescap.org/sites/default/files/09_PE_I.pdf
- [69] S. Harding, "What is binning? a basic definition," Oct 2018. [Online]. Available: https://www.tomshardware.com/reviews/glossarybinning-definition,5892.html

- [70] N. Hemsoth, "Amd on why chiplets—and why now." [Online]. Available: https://www.nextplatform.com/2021/06/09/amd-on-whychiplets-and-why-now/
- [71] Hutcheson, "Remarks at sia event: Big opportunities, looming challenges: The state of the u.s. semiconductor industry." 2020.
- [72] A. Kannan, N. E. Jerger, and G. H. Loh, "Enabling interposer-based disintegration of multi-core processors," in 2015 48th Annual IEEE/ACM International Symposium on Microarchitecture (MICRO). IEEE, 2015, pp. 546–558.
- [73] B. Lee, "Semiconductors: The Supply and Demand Fluctuations | Suntsu News," Jun. 2022. [Online]. Available: https://suntsu.com/2022/06/15/semiconductors-the-supply-anddemand-fluctuations/
- [74] H. L. Lee, "Aligning supply chain strategies with product uncertaintiesl California Management Review. Spring2002, Vol. 44 Issue 3, p105-119."
- [75] I. Lindo Systems, "Lingo 16.0—optimization modeling software for linear, nonlinear, and integer programming," 2017.
- [76] K. Lyons, "Us tightens trade restrictions on chinese chipmaker smic." [Online]. Available: https://www.theverge.com/2020/9/26/21457350/us-tightens-traderestrictions-china-chipmaker-smic
- [77] D. Martin, "Semiconductor average lead time breaks half-year barrier," Apr 2022. [Online]. Available: https://www.theregister.com/2022/04/14/semiconductor_lead_times/
- [78] S. Naffziger, N. Beck, T. Burd, K. Lepak, G. Loh, M. Subramony, and S. White, "Pioneering chiplet technology and design for the amd epyc[™] and ryzen[™] processor families : Industrial product," 06 2021, pp. 57–70.
- [79] S. Pal, D. Petrisko, R. Kumar, and P. Gupta, "Design space exploration for chiplet-assembly-based processors," *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, vol. 28, no. 4, pp. 1062–1073,

2020.

- [80] G. Pantuso and T. K. Boomsma, "On the number of stages in multistage stochastic programs," *Annals of Operations Research*, vol. 292, no. 2, pp. 581–603, 2020.
- [81] J. L. Person, "Computer chips face toilet paper hoarding moment as shortage turns to glut," Jul 2022. [Online]. Available: https://www.reuters.com/technology/computer-chips-face-toiletpaper-hoarding-moment-shortage-turns-glut-2022-07-12/
- [82] A. Shilov, "Amd expected to become tsmc's second largest customer," Mar 2021. [Online]. Available: https: //www.tomshardware.com/news/amd-tsmc-second-largest-customer
- [83] C. Sweeny, "Texas storm offers glimpse of how climate change threatens public health," Feb 2021. [Online]. Available: https://www.hsph.harvard.edu/news/features/texas-storm-offersglimpse-of-how-climate-change-threatens-public-health/
- [84] S. E. I. Technology and I. F. (ITIF), "An allied approach to semiconductor leadership," pp. 1–57. [Online]. Available: https://titf.org/publications/2020/09/17/allied-approachsemiconductor-leadership
- [85] E. Udin, "The shutdown of tsmc's 14a fab will cause losses of over \$28 million," Apr 2021. [Online]. Available: https://www.gizchina.com/2021/04/17/the-shutdown-of-tsmcs-14afab-will-cause-losses-of-over-28-million/
- [86] A. Venkat and D. M. Tullsen, "Harnessing isa diversity: Design of a heterogeneous-isa chip multiprocessor," in 2014 ACM/IEEE 41st International Symposium on Computer Architecture (ISCA). IEEE, 2014, pp. 121–132.
- [87] M. Wayland, "How covid led to a \$60 billion global chip shortage for the auto industry," Feb 2021. [Online]. Available: https://www.cnbc.com/2021/02/11/how-covid-led-to-a-60-billionglobal-chip-shortage-for-automakers.html